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APPLICANTS

Richard K. Williams, Cupertino, CA;
 Michael E. Cornell, Campbell, CA;
 Wai Tien Chen, Hong Kong, HONG KONG;

** CONTINUING DATA *****
 This application is a CON of 10/262,567 09/29/2002 PAT 6,855,985

** FOREIGN APPLICATIONS *****

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature _____ Initials _____	STATE OR COUNTRY CA	SHEETS DRAWING 219	TOTAL CLAIMS 1	INDEPENDENT CLAIMS 1
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ADDRESS
 34036
 SILICON VALLEY PATENT GROUP LLP
 2350 MISSION COLLEGE BOULEVARD
 SUITE 360
 SANTA CLARA , CA
 95054

TITLE
 Modular Bipolar-CMOS-DMOS analog integrated circuit and power transistor technology

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